EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	7470	(boot initialize initialization) same (processors cpus microcontrollers microprocessors)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:30
L3	88	2 and (lockstep)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:30
L4	27	(lockstep same (initialization boot) same (processors CPUS microcontrollers microcomputers))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:38
L5	0	4 not 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:38
L6	217	(redundant redundancy) same (cpus processors computers microcontrollers microcomputers) same (boot initialization initialize)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:39
L7	10	(redundant redundancy) same (cpus processors computers microcontrollers microcomputers) same (boot initialization initialize) and ice	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:41
L8	7	(redundant redundancy) same (cpus processors computers microcontrollers microcomputers) same (boot initialization initialize) and emulator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:50
L9	28	(lockstep) same (processors cpus) same (boot initialize initialization bootstrap)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/14 16:50

| L10 | 2 | (lockstep) same (execution) same (boot initialization initialize bootstrap) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2009/04/14 16:52 |

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